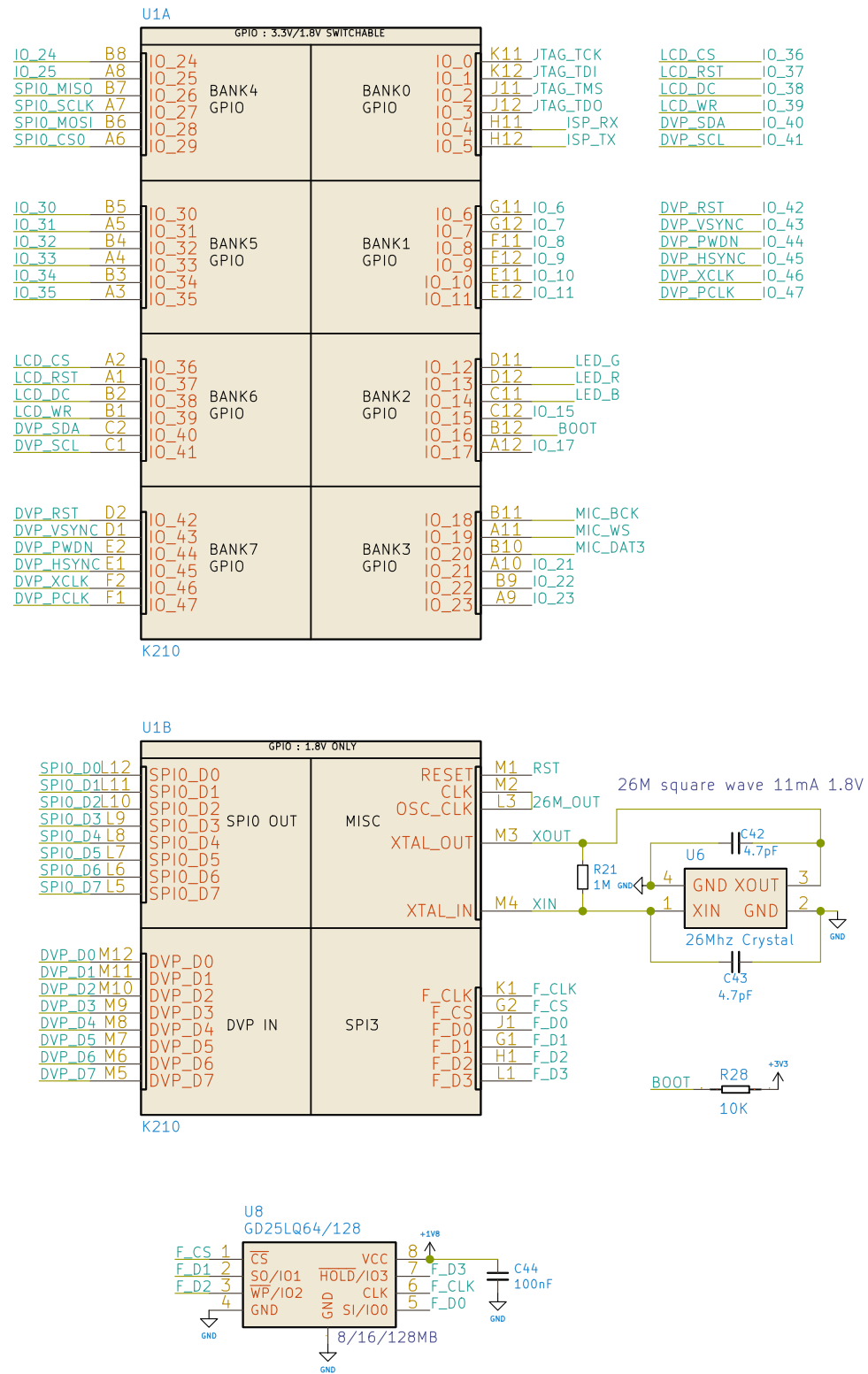
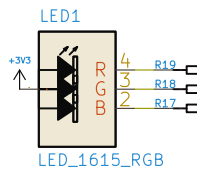


Core System

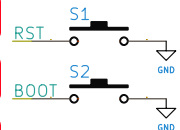


Note:
 BOOT(IO16) pull low to enter ISP mode, download from IO4,5
 SPI3 Support XIP SPI0,1/3 for Master, SPI2 for slave
 3.3V IO speed 80M, 1.8V IO speed 208M. Simple test PWM is faster than 300MHz.

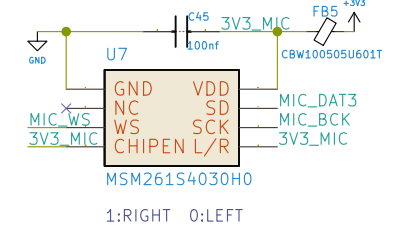
RGB LED



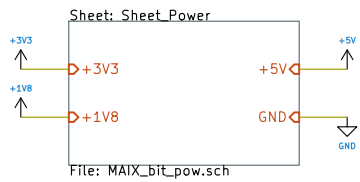
KEY



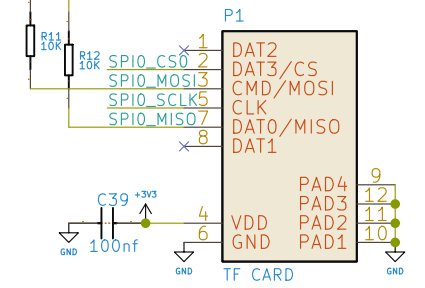
MIC



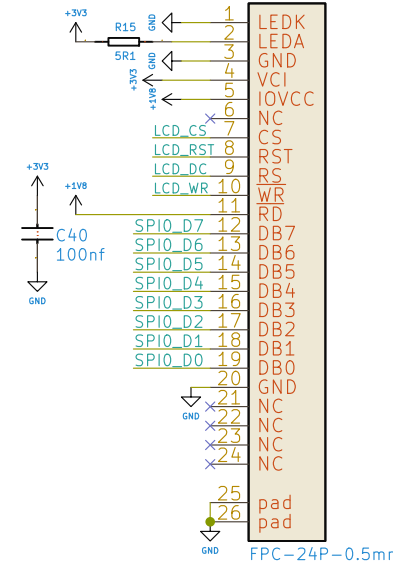
Power



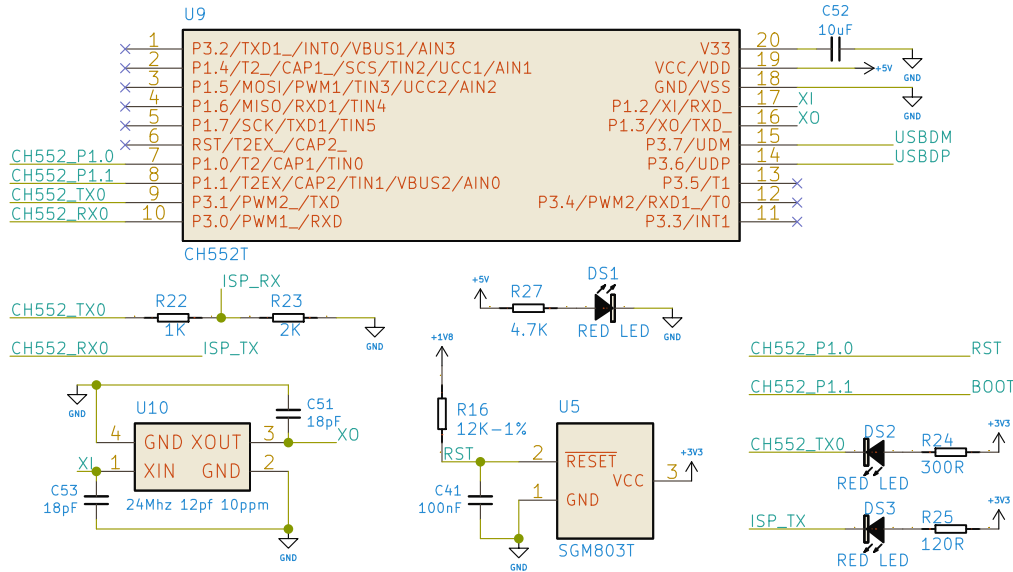
TF Card



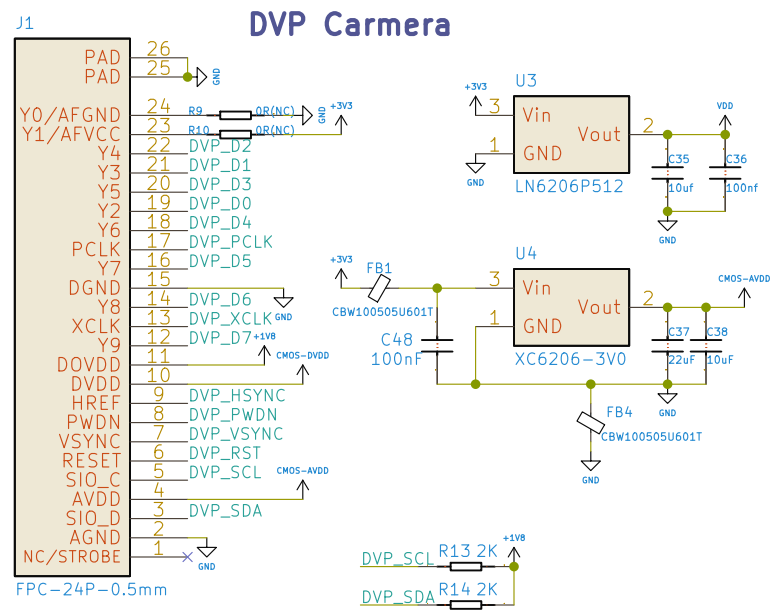
8bit MCU LCD



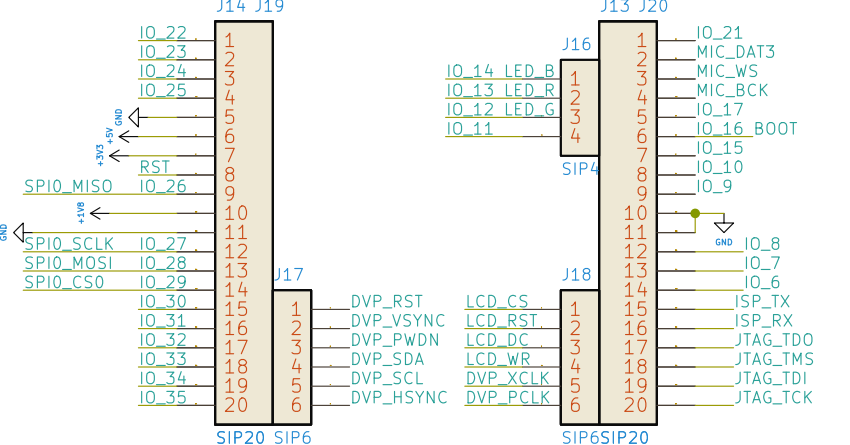
ISP DOWNLOAD



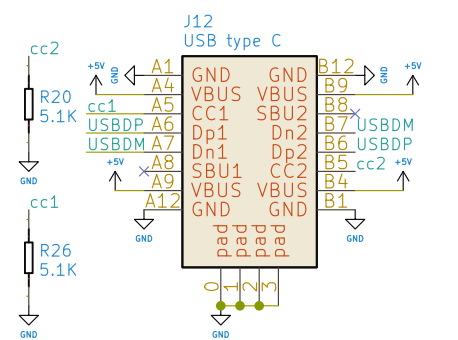
DVP Camera



Connector

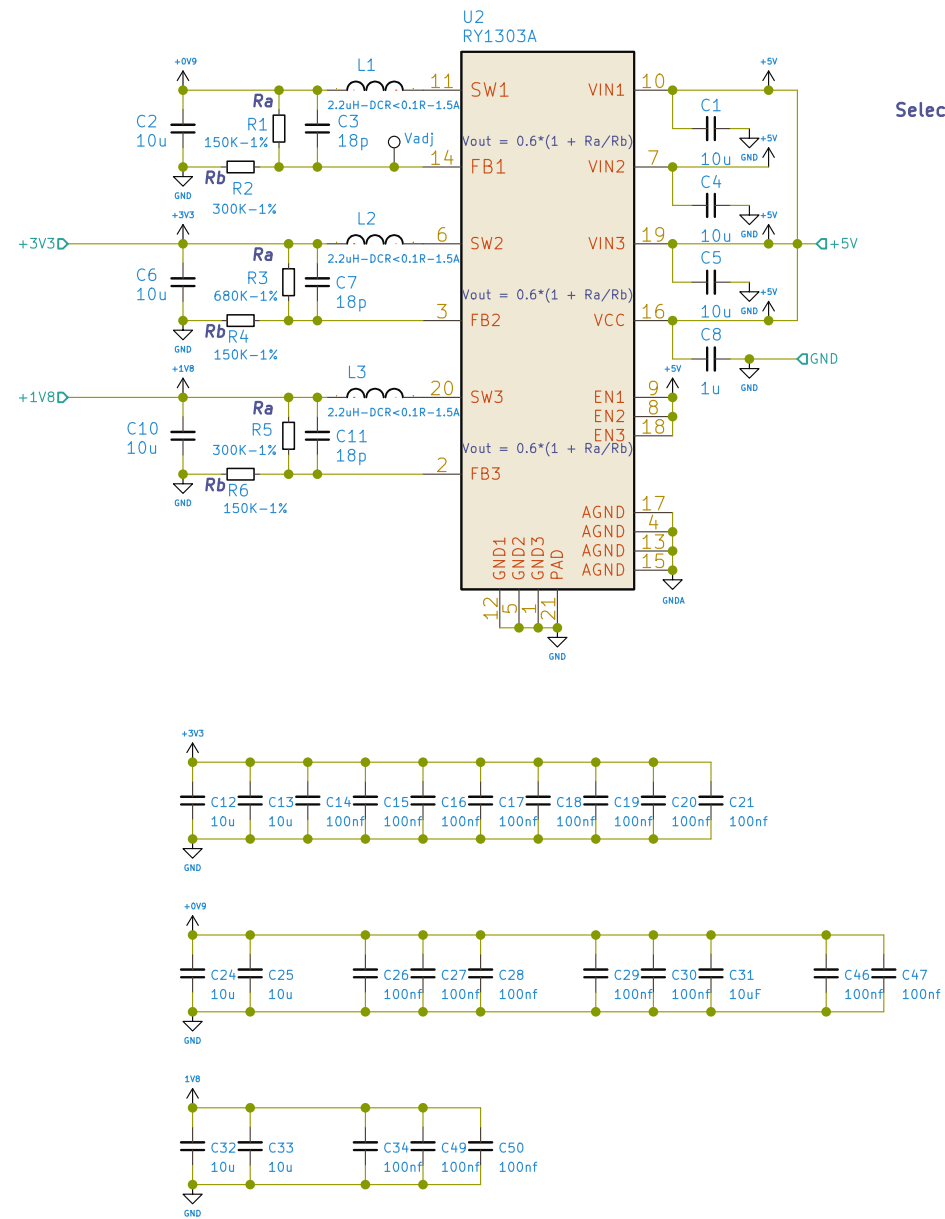


USB Port

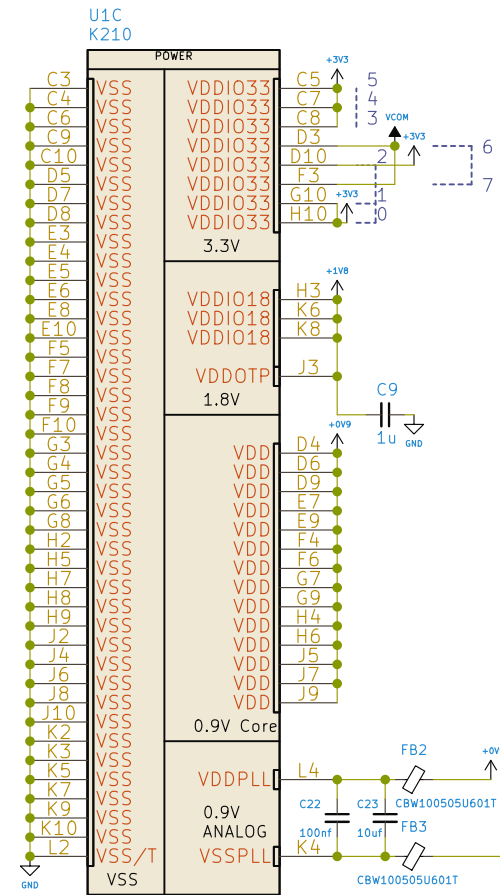
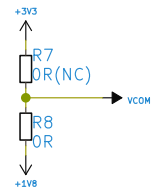


S
 Open-source hardware with MaixPy, A.I. at edge.
Shenzhen Sipeed Technology Co. Ltd.
 Sheet: /
 File: MAIX_Bit_2832.sch
Title: Maix-Bit V2.0(Schematic)
 Size: B Date: 2020-01-18 Rev: 2.0
 KiCad E.D.A. eeschema (5.1.6)-1 Id: 1/2

DCDC&LDO



Select bank 6,7 Voltage



Open-source hardware with MaixPy. A.I. at edge.

Shenzhen Sipeed Technology Co., Ltd.

Sheet: /Sheet_Power/

File: MAIX_bit_pow.sch

Title: **Maix-Bit V2.0(Schematic)**

Size: B

Date: 2020-01-18

Rev: 2.0

KiCad E.D.A. eeschema (5.1.6)-1

Id: 2/2