

Revision History

Version	Date	Change Note
3708	2022/07/21	First release version.
3709	2022/09/01	Change the DOVDD of DVP from 3.3V to 2.5V.
3711	2022/10/30	Fixed footprint issue for WS2812C.
3713	2023/09/12	Anniversary Version, fixed PMOD spacing issue.
3714	2026/05/20	DVI optimize, SW0 and KEY4 pins were swapped

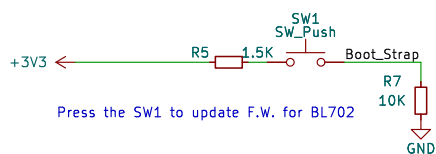


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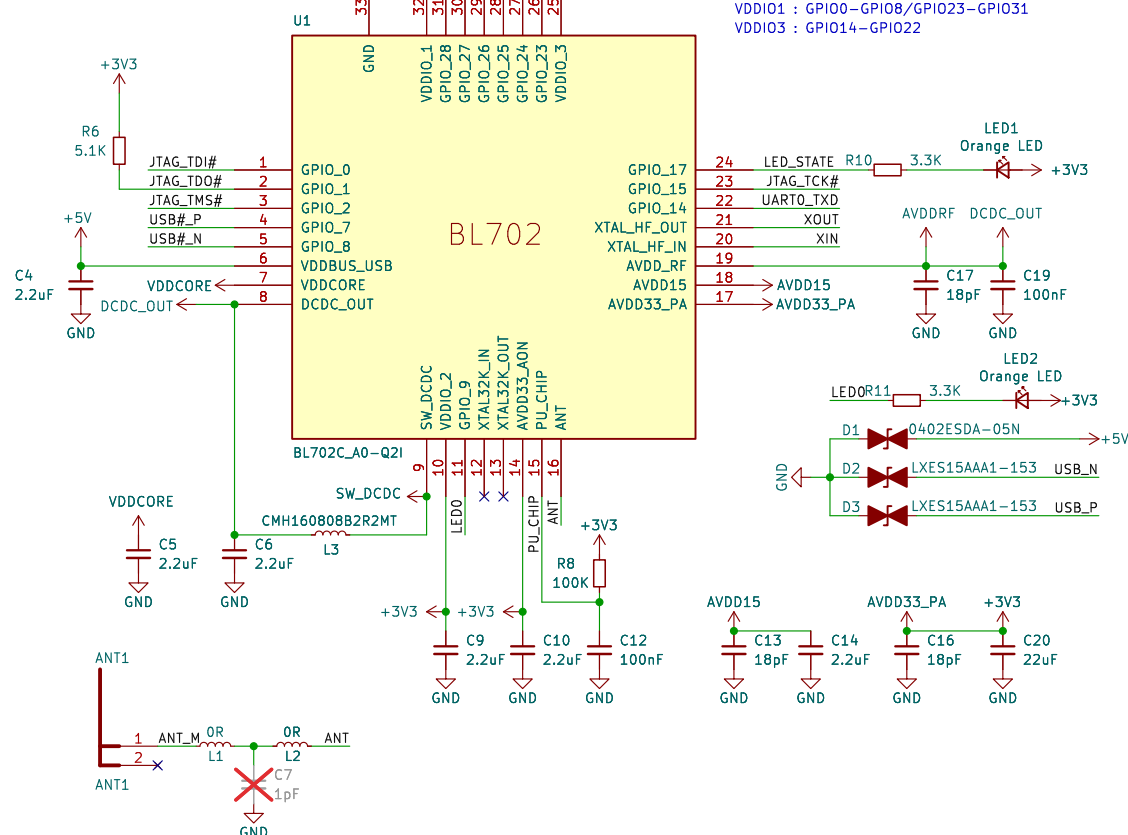
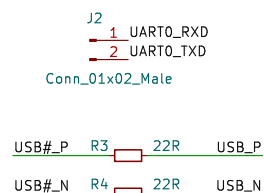
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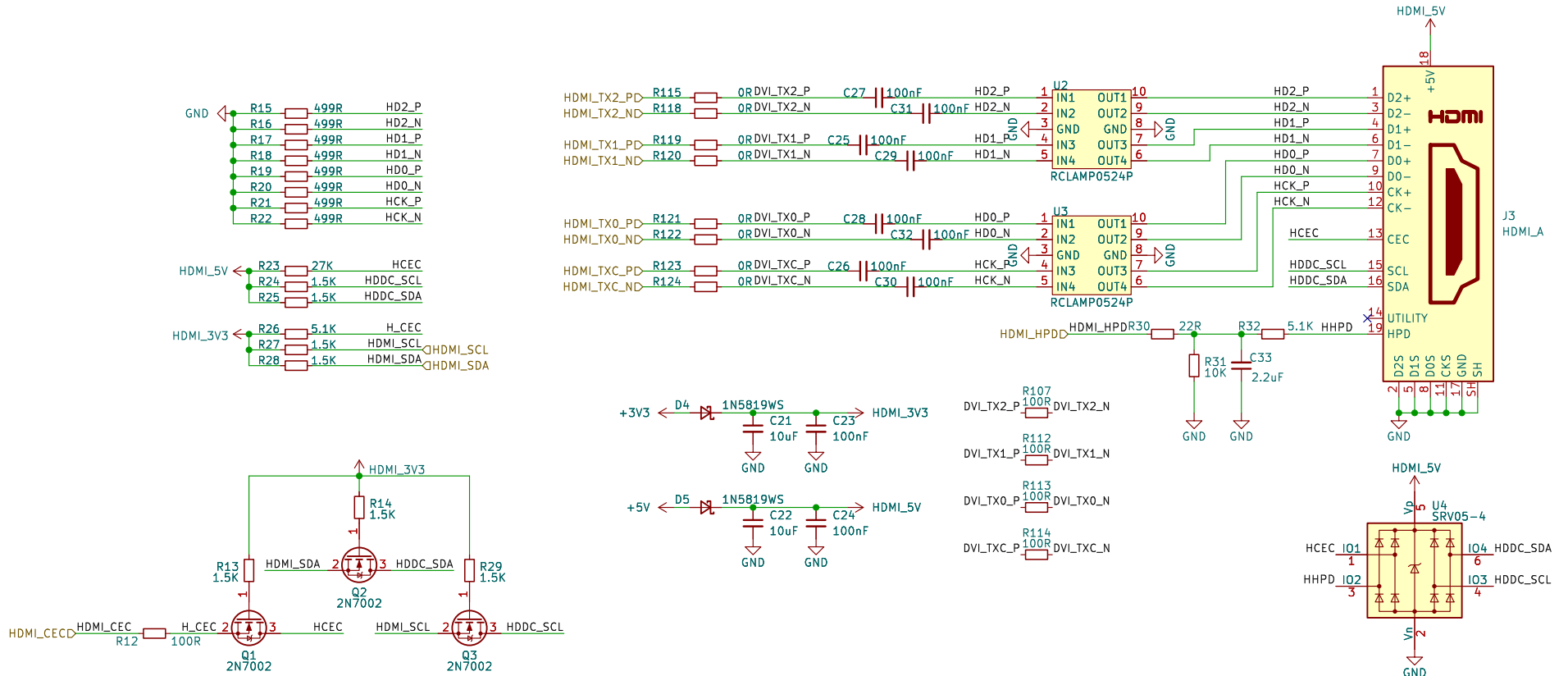
Size: A3	Date: 2026-05-20	Rev: 1.3
KiCad E.D.A. 9.0.9		Id: 1/9



BANK2 VOLTAGE is locked to 3V3(JTAG IO)
3.3V default (Not recommended to change)



HDMI_TX/RX



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Sheet: /P004_HDMI/
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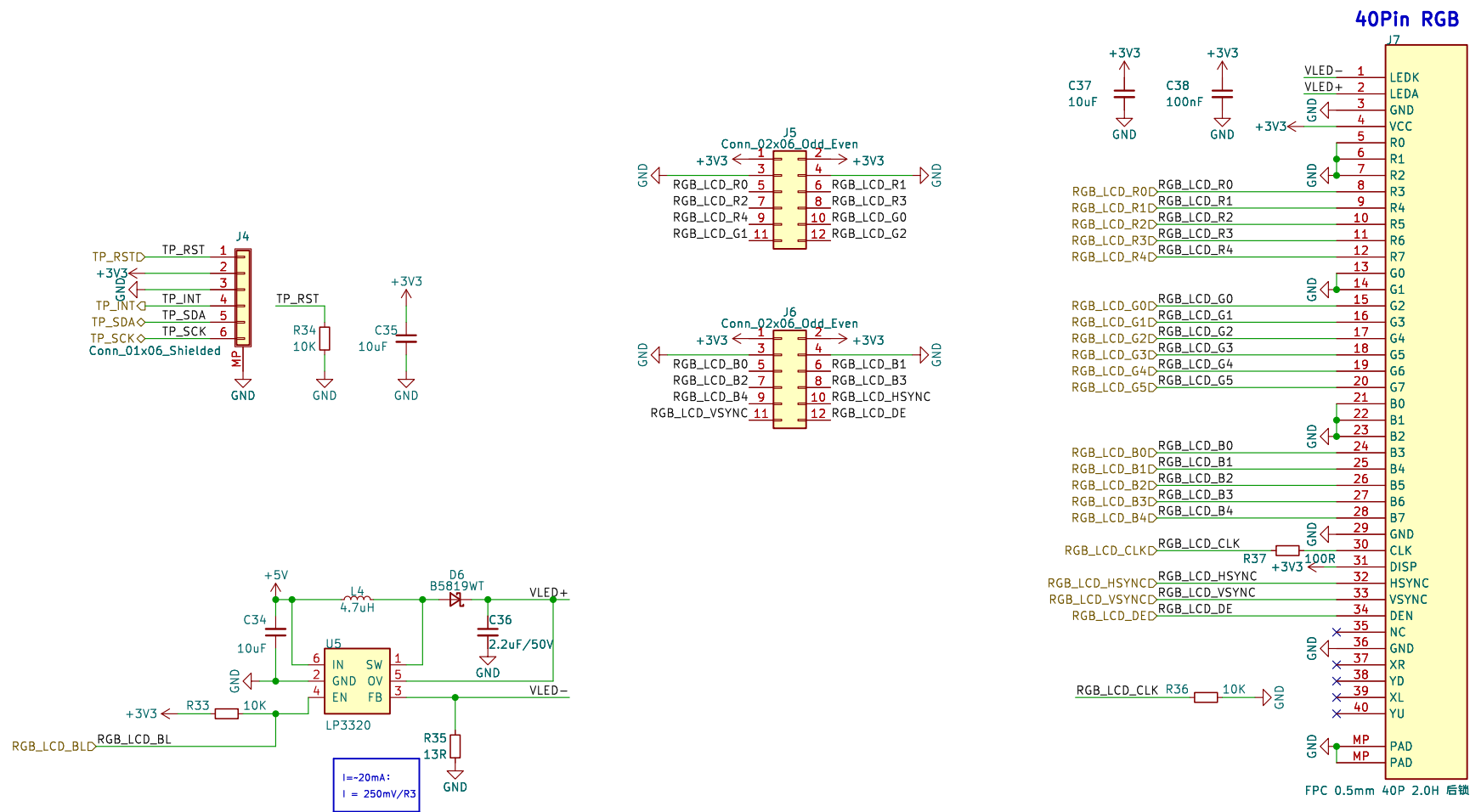
Size: A4 Date: 2023-09-12

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Rev: 1.1

Id: 5/9

RGB LCD



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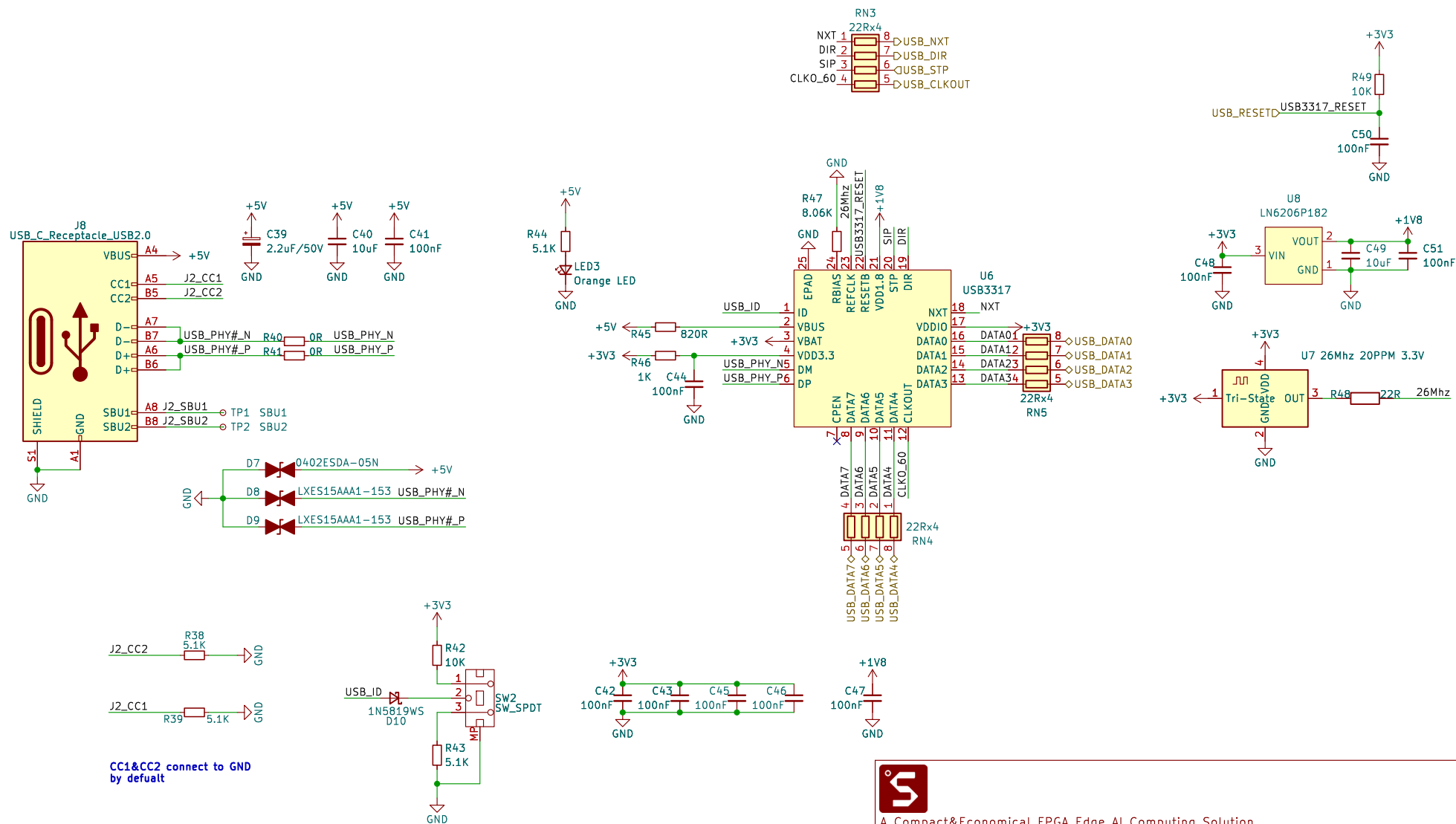
Size: A4	Date: 2023-09-12
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Rev: 1.1

Id: 6/9

USB 2.0 PHY-IN BANK7



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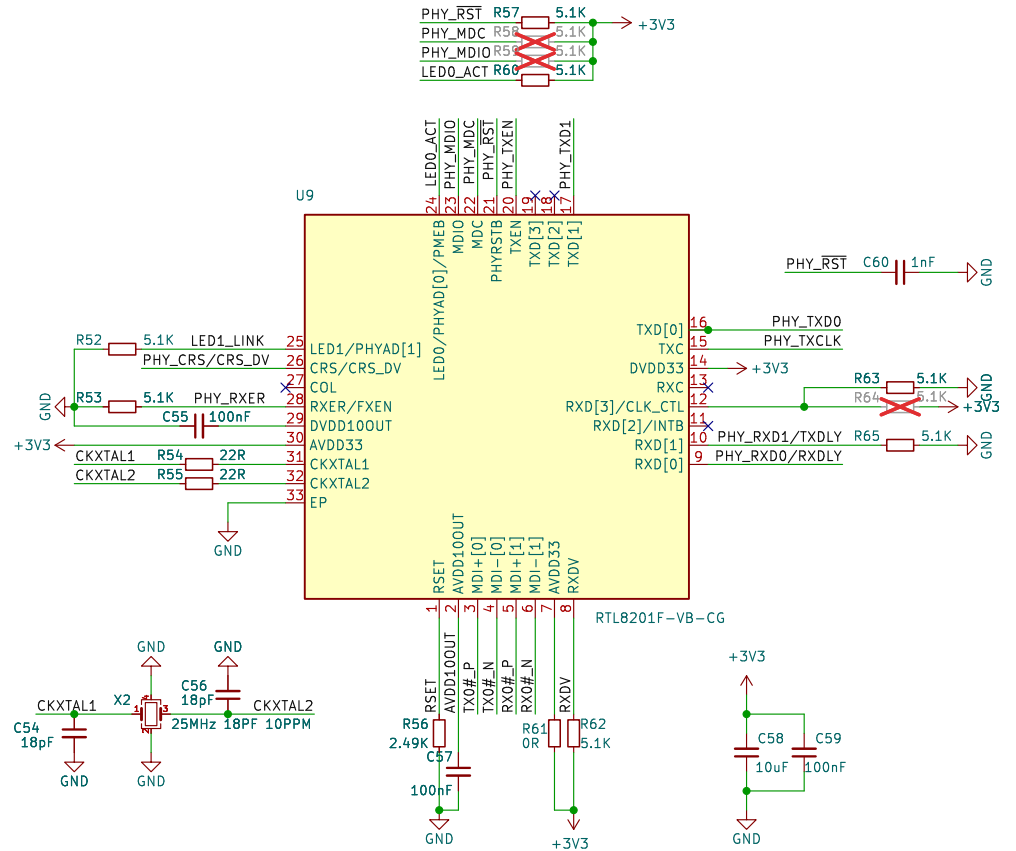
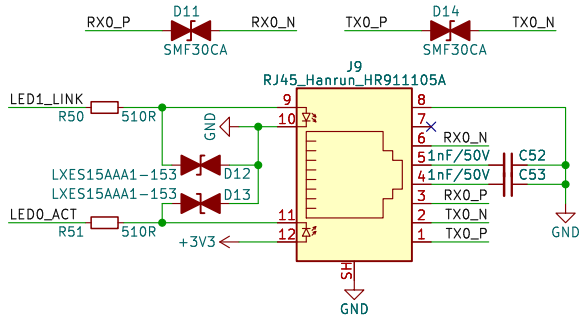
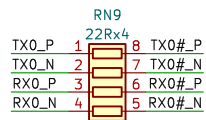
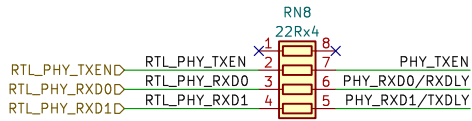
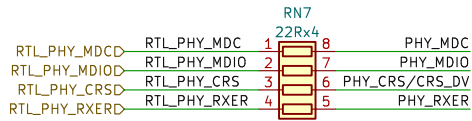
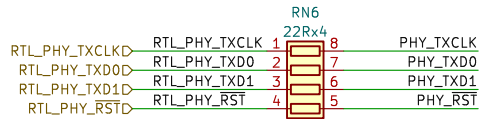
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Rev: 1.1

Id: 7/9



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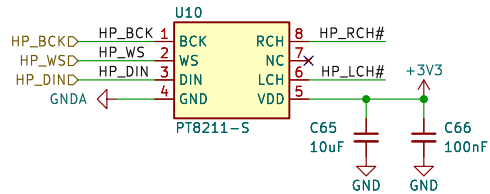
KiCad E.D.A. 9.0.9

Rev: 1.1

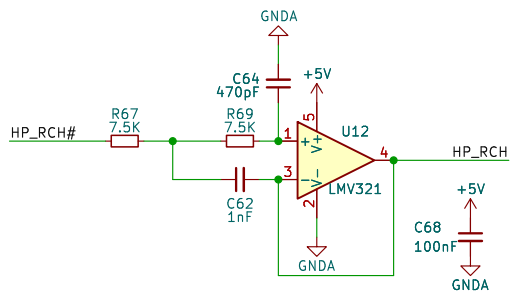
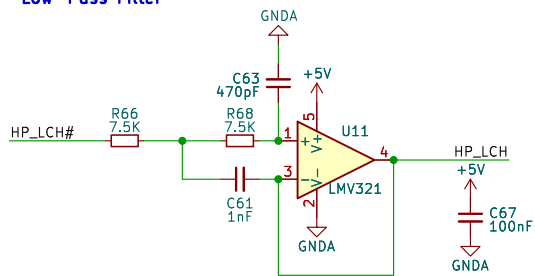
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Audio

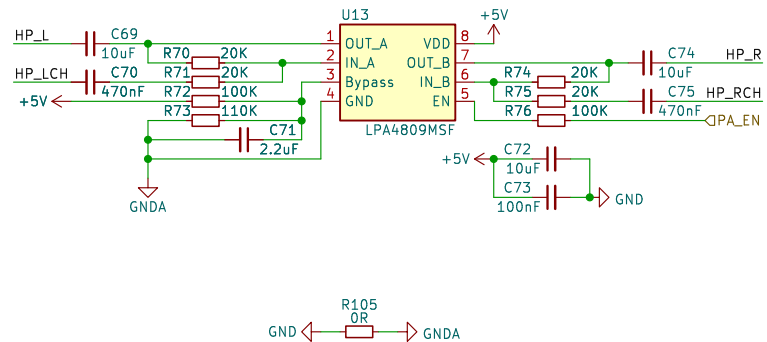
STEREO DAC



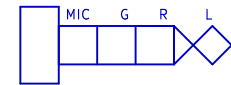
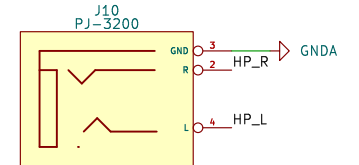
Low-Pass Filter



HEADPHONE AMP.



HEADPHONE JACK 3.5MM



When the jack is unplugged, L/R_DET is connected to L/R.



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Size: A4	Date: 2023-09-12
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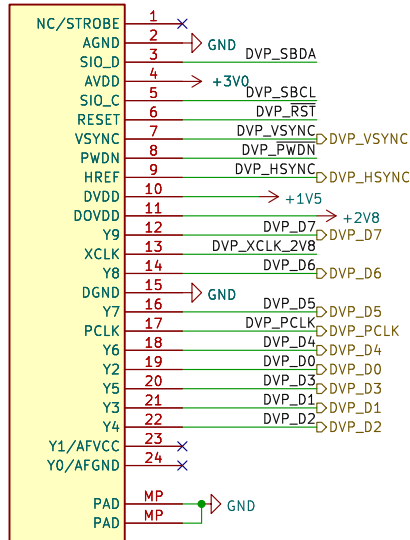
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Rev: 1.1

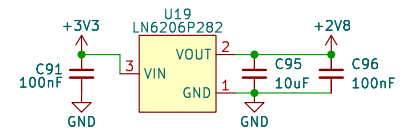
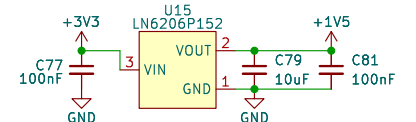
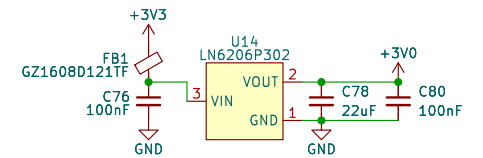
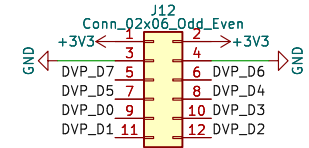
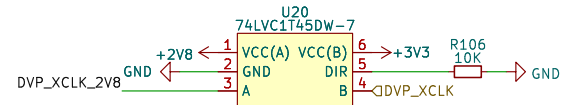
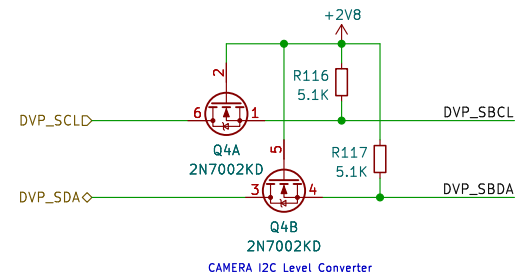
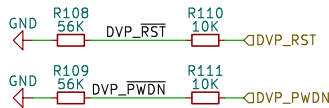
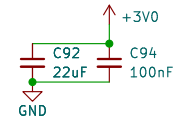
Id: 8/9

DVP Carmera

J11
FPC 0.5mm 24P 2.0H backlock



NOTICE:
DOVDD refers to I/O Bank voltage



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Sheet: /008_Carmera/
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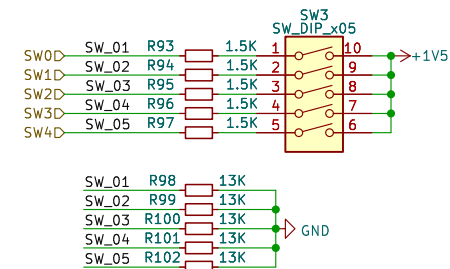
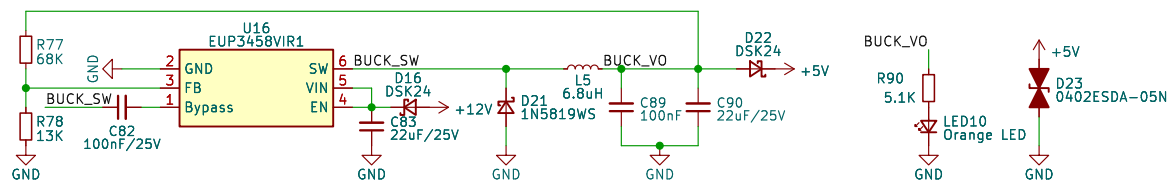
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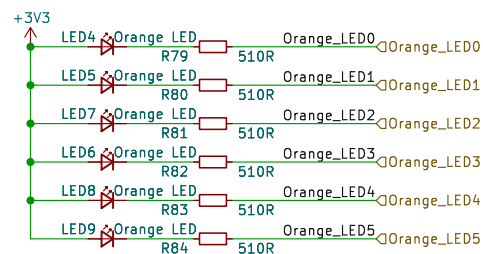
Rev: 1.1

Id: 9/9

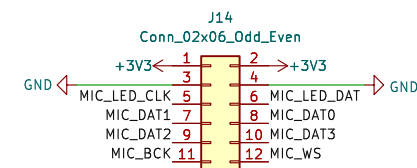
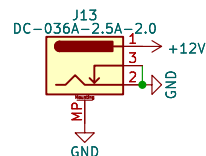
DC-IN BUCK



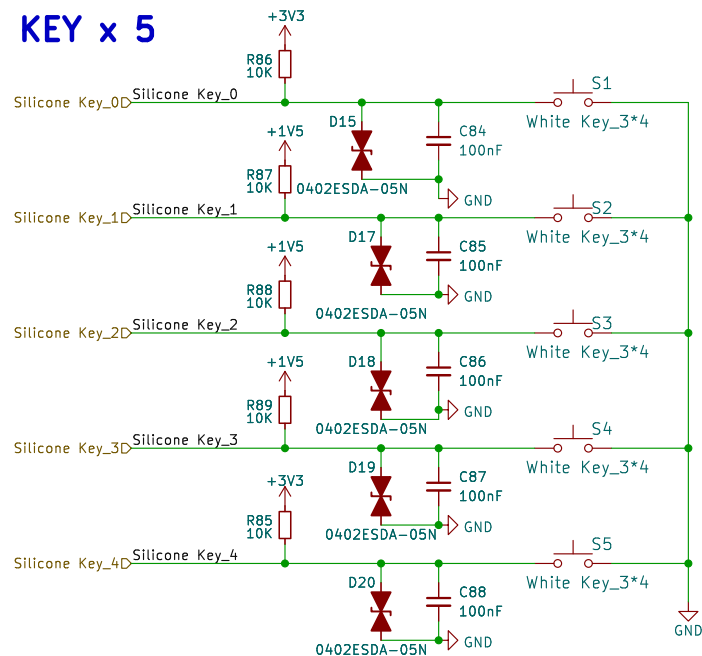
LED x 6



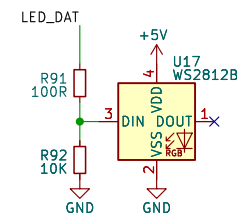
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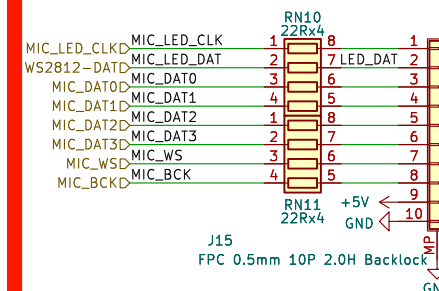
KEY x 5



WS2812B



MIC Array Conn.



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Title: Tang_Primer_20K-Dock_3713

Size: A4 Date: 2023-09-12

KiCad E.D.A. 9.0.9

Rev: 1.1

Id: 9/9